PRESS RELEASE

Fraunhofer HHI is expanding its product portfolio of IP-cores for real-time signal processing in digital communication systems

High-performance IP-cores are essential for short development cycles in the area of real-time signal processing for digital communication systems. The Fraunhofer Heinrich Hertz Institute HHI is now making its know-how in the area of real-time signal processing for digital communication systems available to its customers.

The new 10G Ethernet MAC IP-core is one of the fastest IP-cores currently available on the market. It was specifically designed to achieve minimal latency as well as minimal resource consumption. This makes it ideal for any application with demanding latency requirements, such as systems for digital data processing, the tactile Internet, or high-frequency trading.

The platform-independent 10G Ethernet MAC IP-core can be used on all important FPGA-platforms (Xilinx Virtex 5/6/7, Xilinx Spartan 6, Altera Stratix 4/5). It complies with the IEEE802.3-2008 standard. Upon customer request, the architecture of the IP-core is scalable to higher data rates for 40/100G Ethernet applications and can be diversely configured.

The data sheet can be found under www.hhi.fraunhofer.de/ip-cores.

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The Fraunhofer Heinrich Hertz Institute is a world leader in the development of mobile and fixed broadband communication networks and multimedia systems. From photonic components and systems through fiber optic sensor systems to video coding and transmission, the Fraunhofer HHI works together with its international partners from research and industry. www.hhi.fraunhofer.de